

Wide input voltage, Non-isolated and regulated single output

FEATURES

- High efficiency up to 95%
- No-load input current as low as 0.2mA
- Operating ambient temperature range: -40°C to +85°C
- Output short-circuit protection
- SMD package



CE Report
EN 62368-1

UKCA Report
BS EN 62368-1

RoHS

K78_T-1000R3 series are high efficiency switching regulators. The converters feature high efficiency, low loss and short circuit protection in a compact SMD package. These products are widely used in applications such as industrial control, instrumentation and electric power.

Selection Guide

Certification	Part No.	Input Voltage (VDC)*	Output		Full Load Efficiency (%) Typ. Vin Min. / Vin Max.	Capacitive Load (µF) Max.
		Nominal (Range)	Voltage (VDC)	Current (mA) Max.		
EN/BS EN	K7801T-1000R3	12 (4.75-32)	1.5	1000	76/66	680
	K78X2T-1000R3	12 (4.75-32)	1.8	1000	79/69	680
	K7802T-1000R3	12 (4.75-32)	2.5	1000	86/74	680
	K7803T-1000R3	24 (6.5-36)	3.3	1000	90/80	680
	K7805T-1000R3	24 (8-36)	5	1000	93/85	680
	K78X6T-1000R3	24 (10-36)	6.5	1000	93/86	680
	K7809T-1000R3	24 (13-36)	9	1000	94/89	680
	K7812T-1000R3	24 (16-36)	12	800	95/92	680

Note:*For input voltage exceeding 30 VDC, an input capacitor of 22µF/50V is required.

Input Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
No-load Input Current		--	0.2	1	mA
Reverse Polarity at Input		Avoid / Not protected			
Input Filter		Capacitance filter			
Ctrl	Module on	Open or pulled high (TTL level 3.2-5.5VDC)			
	Module off	Pulled low to GND level (0-0.8VDC)			
	Input current when off	--	0.2	1	mA

Output Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit	
Voltage Accuracy	Full load, input voltage range	1.5/1.8/2.5/3.3VDC output	--	±2	±4	%
		Other output	--	±2	±3	
Linear Regulation	Full load, input voltage range	1.5/1.8/2.5VDC output	--	±0.3	±0.6	
		Other output	--	±0.2	±0.4	
Load Regulation	Nominal input voltage, 10% -100% load	1.5/1.8/2.5VDC output	--	0.8	±1.5	
		Other output	--	0.3	±0.6	

Ripple & Noise*	20MHz bandwidth	--	30	75	mVp-p
Temperature Coefficient	Operating temperature range -40°C to +85°C	--	--	±0.03	%/°C
Transient Response Deviation	Nominal input voltage, 25% load step change	--	50	150	mV
Transient Recovery Time		--	0.2	1	ms
Short-circuit Protection	Nominal input	Continuous, self-recovery			
Vadj	Input voltage range	-10	--	+10	%Vo

Note: *

① The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information;

② With light loads at or below 20%, Ripple & Noise increases to 150mVp-p max.

General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	单位	
Operating Temperature	See Fig. 1	-40	--	+85	°C	
Storage Temperature		-55	--	+125		
Storage Humidity	Non-condensing	5	--	95	%RH	
Reflow Soldering Temperature		Peak temperature ≤245°C, duration ≤60s max. over 217°C. Also refer to IPC/JEDEC J-STD-020D.1.				
Switching Frequency	Full load, nominal input	1.5/1.8/2.5VDC output	--	370	--	kHz
		3.3/5/6.5VDC output	--	520	--	
		9/12VDC output	--	700	--	
MTBF	MIL-HDBK-217F@25°C	2000	--	--	k hours	
Moisture Sensitivity Level (MSL)*	IPC/JEDEC J-STD-020D.1	Level 1				

Note: * For actual application, please refer to IPC/JEDEC J-STD-020D.1.

Mechanical Specifications

Case Material	Black plastic; flame-retardant and heat-resistant (UL94V-0)
Dimensions	15.24 x 11.40 x 8.25 mm
Weight	1.7g (Typ.)
Cooling Method	Free air convection

Electromagnetic Compatibility (EMC)

Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 4-② for recommended circuit)	
	RE	CISPR32/EN55032	CLASS B (see Fig. 4-② for recommended circuit)	
Immunity	ESD	IEC/EN 61000-4-2	Contact ±4kV	perf. Criteria B
	RS	IEC/EN 61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN 61000-4-4	±1kV (see Fig. 4-① for recommended circuit)	perf. Criteria B
	Surge	IEC/EN 61000-4-5	line to line ±1kV (see Fig. 4-① for recommended circuit)	perf. Criteria B
	CS	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A

Typical Characteristic Curves

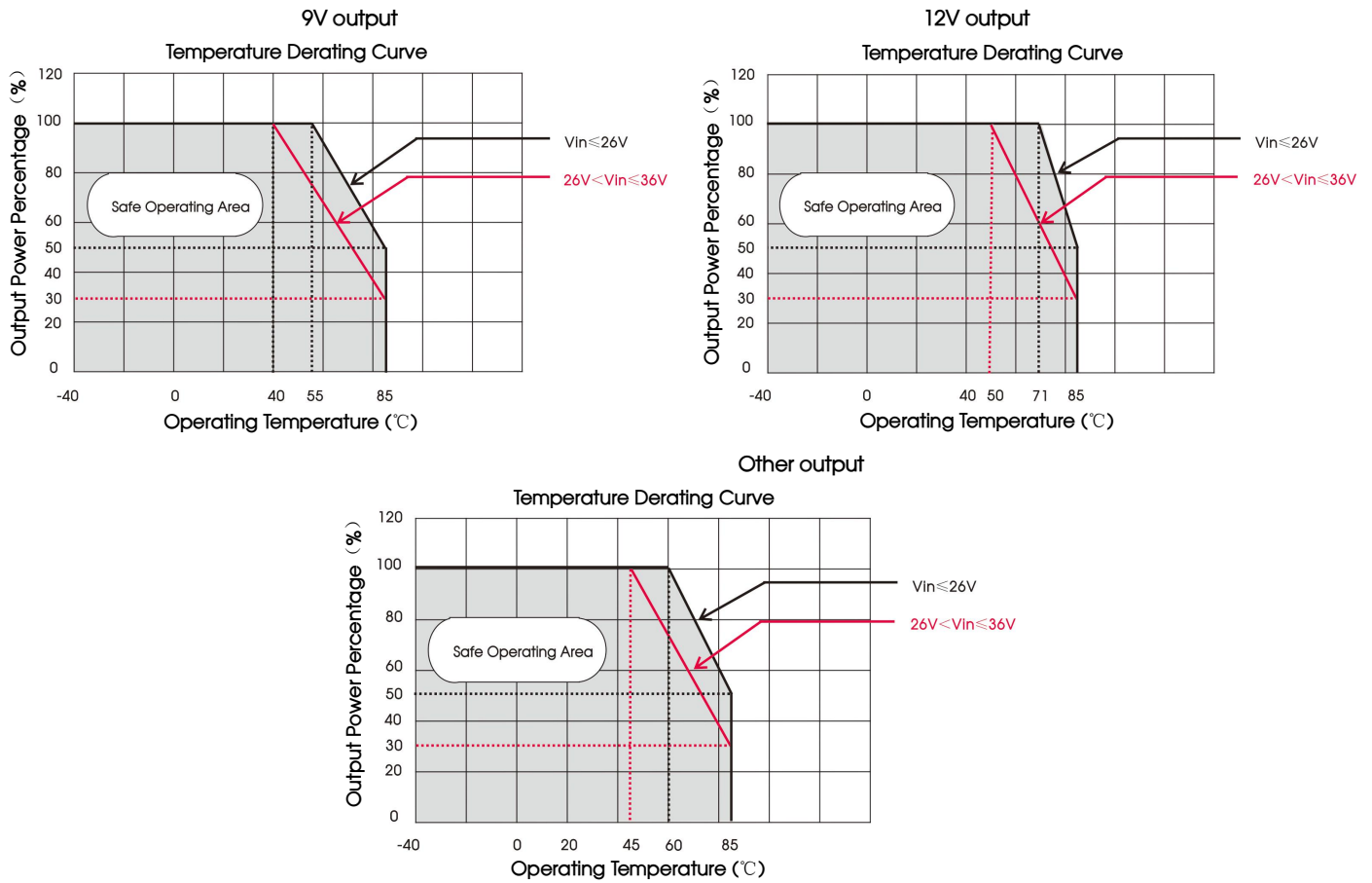
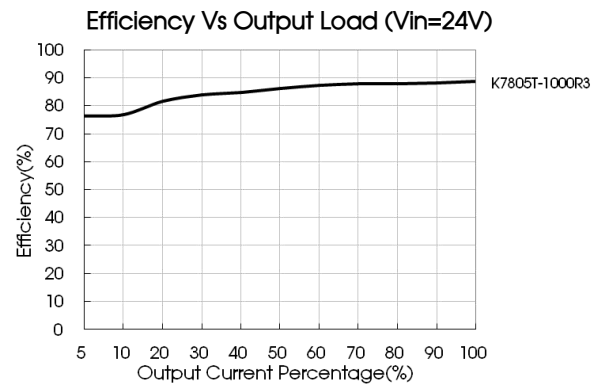
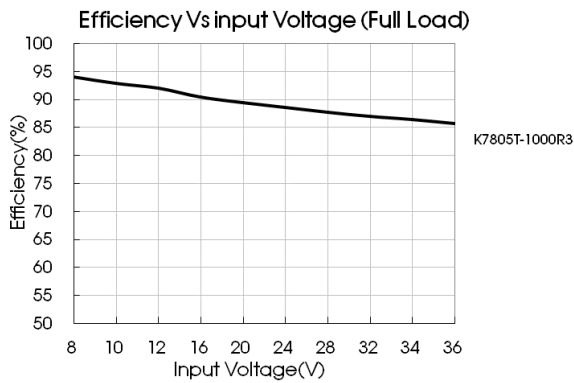


Fig. 1



Design Reference

1. Typical application

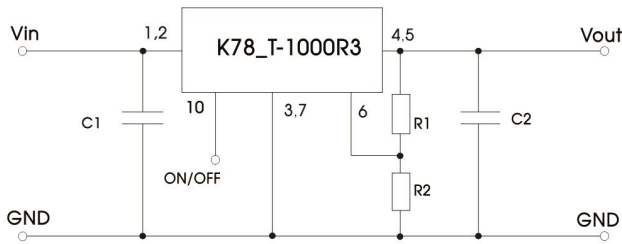


Fig. 2 Typical application circuit

Part No.	C1 (ceramic capacitor)	C2 (ceramic capacitor)	R1/R2 (Vadj resistance)
K7801T-1000R3	10μF/50V	22μF/10V	Refer to Vadj resistance calculation
K78X2T-1000R3		22μF/10V	
K7802T-1000R3		22μF/10V	
K7803T-1000R3		22μF/10V	
K7805T-1000R3		22μF/16V	
K78X6T-1000R3		22μF/16V	
K7809T-1000R3		22μF/16V	
K7812T-1000R3		22μF/25V	

table 1

- Note:
- The required C1 and C2 capacitors must be connected as close as possible to the terminals of the module.
 - Refer to Table 1 for C1 and C2 capacitor values. For certain applications, increased values and/or tantalum or low ESR electrolytic capacitors may also be used instead.
 - Converter cannot be used for hot swap and with output in parallel.
 - To further reduce the output ripple and noise, we suggested the use of a "LC" filter at the output terminals, with an inductor value (L) of 10μH-47μH.

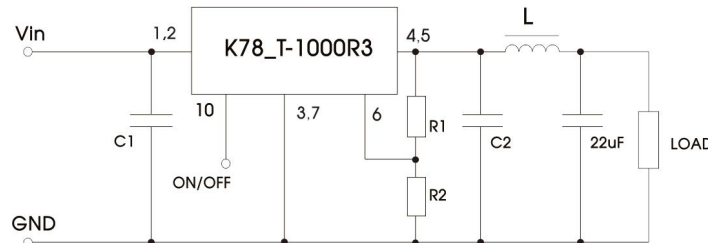


Fig. 3 External "LC" output filter circuit diagram

2. EMC compliance circuit

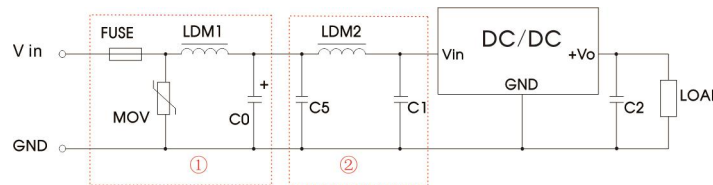


Fig.4 Recommended compliance circuit

FUSE	MOV	LDM1	C0	C2	C1/C5	LDM2
Select fuse value according to actual input current	S20K30	82μH	680μF /50V	Refer to table 1	4.7μF /50V	68μH

Note: Part ① in Fig. 4 shows Immunity compliance filter and part ② filter for Emission compliance; depending on requirement both filters ① and ② can be used in series as shown.

3. Trim Function for Output Voltage Adjustment (open if unused)

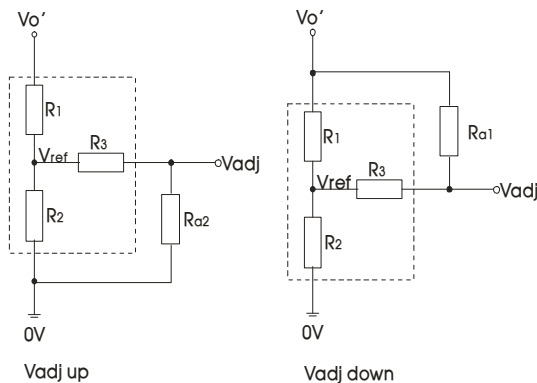


Fig.5 Circuit diagram of Vadj up and down (dashed line shows internal part of module)

Calculating Trim resistor values:

$$\begin{aligned} \text{up: } R_{a2} &= \frac{\alpha R_2}{R_2 - \alpha} - R_3 & \alpha &= \frac{V_{ref}}{V_o' - V_{ref}} \cdot R_1 \\ \text{down: } R_{a1} &= \frac{\alpha R_1}{R_1 - \alpha} - R_3 & \alpha &= \frac{V_o' - V_{ref}}{V_{ref}} \cdot R_2 \end{aligned}$$

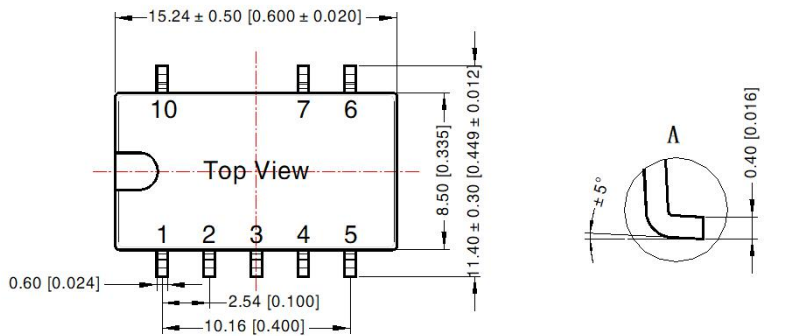
Ra1、Ra2 = Trim Resistor value;
Vo' = desired output voltage;
α = self-defined parameter.

Vout(V)	R1(k Ω)	R2(k Ω)	R3(k Ω)	Vref(V)
1.5	7.5	7.5	15	0.75
1.8	4.7	3.3	6.8	0.75
2.5	9.1	3.9	8.2	0.75
3.3	75	22	75	0.75
5	43	7.5	33	0.75
6.5	43	5.6	22	0.75
9	43	3.9	22	0.75
12	36	2.4	10	0.75

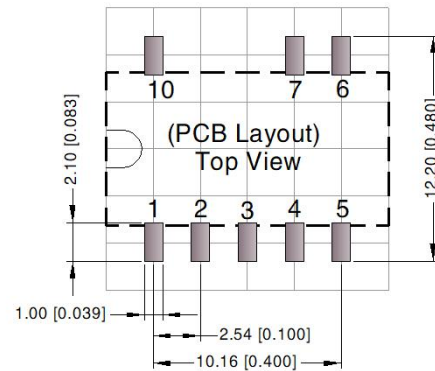
Note: The 1.5V model's output voltage can only be adjusted up (Vadj up) and cannot be adjusted to a lower voltage (Vadj down is not applicable).

4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com

Dimensions and Recommended Layout



THIRD ANGLE PROJECTION



Note: Grid 2.54*2.54mm

Pin-Out	
Pin	Mark
1	+Vin
2	+Vin
3	GND
4	+Vout
5	+Vout
6	V adj
7	GND
10	Remote On/Off

Note:
Unit: mm[inch]
Pin section tolerances: ± 0.10 [± 0.004]
General tolerances: ± 0.25 [± 0.010]

NC: Pin to be isolated from circuitry

Notes:

1. For additional information on Product Packaging please refer to www.mornsun-power.com. Tape Packaging bag number: 58210057, Roll packaging bag number: 58210058;
2. The max. capacitive load should be tested within the input voltage range and under full load conditions;
3. Unless otherwise specified, data in this datatable should be tested under the conditions of $T_a=25^{\circ}\text{C}$, humidity<75%RH when inputting nominal voltage and outputting rated load;
4. All index testing methods in this datatable are based on our company corporate standards;
5. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact with our technician for specific information;
6. Products are related to laws and regulations: see "Features" and "EMC";
7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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